Freeform Search

Database	US Pre-Grant Publica US Patents Full-Text US OCR Full-Text Da EPO Abstracts Datab JPO Abstracts Datab Derwent World Paten	Database Itabase ase ase							
Term:	erm: US-6523077-B1.did.								
Display: 10 Documents in Display Format: TI Starting with Number									
Generate: O Hit List O Hit Count O Side by Side O Image									
Search Clear Interrupt									
		Search History	y						
ATE: Sa	nturday, June 14, 2008	Search History Purge Queries	Printable Copy	Create Case					
Set Name	Query		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count S					
Set Name ide by side DB=USI	Query		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count S	Set Name				
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Set Name ide by side DB=USI	Query PT; PLUR=NO; OP=ADJ		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count S	Set Name result set				
Set Name ide by side DB=USI L58	Query PT; PLUR=NO; OP=ADJ US-6523077-B1.did.		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count S	Set Name result set				
Set Name ide by side DB=USI L58 L57 L56 DB=PGI	Query PT; PLUR=NO; OP=ADJ US-6523077-B1.did. US-7050720-B2.did. US-7050720-B2.did. PB; PLUR=NO; OP=ADJ	Purge Queries	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count 5	L58 L57 L56				
Set Name ide by side DB=USI L58 L57 L56 DB=PGI L55	Query PT; PLUR=NO; OP=ADJ US-6523077-B1.did. US-7050720-B2.did. US-7050720-B2.did. PB; PLUR=NO; OP=ADJ US-20020149821-A1.did.	Purge Queries	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count S	Set Name result set 1.58 1.57				
Set Name ide by side DB=USI L58 L57 L56 DB=PGI L55 DB=JPA	Query PT; PLUR=NO; OP=ADJ US-6523077-B1.did. US-7050720-B2.did. US-7050720-B2.did. PB; PLUR=NO; OP=ADJ US-20020149821-A1.did. B; PLUR=NO; OP=ADJ	Purge Queries	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count 9	L58 L57 L56				
Set Name ide by side DB=USI L58 L57 L56 DB=PGI L55	Query PT; PLUR=NO; OP=ADJ US-6523077-B1.did. US-7050720-B2.did. US-7050720-B2.did. PB; PLUR=NO; OP=ADJ US-20020149821-A1.did.	Purge Queries	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Hit Count 5	L58 L57 L56				

DB=PC	GPB; PLUR=NO; OP=ADJ				
<u>L52</u>	US-20040064625-A1.did.	1	<u>L52</u>		
<u>L51</u>	US-20040064625-A1.did.	1	<u>L51</u>		
DB=PC	GPB,USPT; PLUR=NO; OP=ADJ				
<u>L50</u>	(2000293503 or 2002110925 or 2004126677 or 2002238034). pn.	0	<u>L50</u>		
<u>L49</u>	(6523077 or 2000293503 or 2002110925 or 2004126677 or 2002238034).pn.	1	<u>L49</u>		
$DB=D^{\dagger}$	WPI; PLUR=NO; OP=ADJ				
<u>L48</u>	(6523077 or 2000293503 or 2002110925 or 2004126677 or 2002238034).pn.	6	<u>L48</u>		
<u>L47</u>	date.in. and a.in. and canon.as.	60	<u>L47</u>		
<u>L46</u>	date.in. and atushi.in. and canon.as.	0	<u>L46</u>		
<u>L45</u>	date.in. and canon.as. and (core).ti.	0	<u>L45</u>		
<u>L44</u>	date.in. and canon.as. and (cpu core).ti.	0	<u>L44</u>		
<u>L43</u>	date.in. and canon.as.	149	<u>L43</u>		
DB=PGPB,USPT; PLUR=NO; OP=ADJ					
<u>L42</u>	20020056014.pn.	1	<u>L42</u>		
<u>L41</u>	20040064625.uref.	0	<u>L41</u>		
<u>L40</u>	L32 not (L38 or L34)	46	<u>L40</u>		
<u>L39</u>	L34 not L38	40	<u>L39</u>		
<u>L38</u>	L32 and matsushita.as.	6	<u>L38</u>		
<u>L37</u>	osada.in. and matsushita.as.	12	<u>L37</u>		
<u>L36</u>	osada.in. and tokieda.in. and matsushita.as.	0	<u>L36</u>		
DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ					
<u>L35</u>	internal cpu\$1 and external cpu\$1	13	<u>L35</u>		
DB=PGPB,USPT; PLUR=NO; OP=ADJ					
<u>L34</u>	L32 not L33	41	<u>L34</u>		
<u>L33</u>	L30 with L31	51	<u>L33</u>		
<u>L32</u>	L30 same L31	92	<u>L32</u>		
<u>L31</u>	external cpu\$1	2298	<u>L31</u>		
<u>L30</u>	internal cpu\$1	1590	<u>L30</u>		
<u>L29</u>	internal cpu	1575	<u>L29</u>		

<u>L28</u>	external cpu	2244	<u>L28</u>
<u>L27</u>	L26 not L25	3	<u>L27</u>
<u>L26</u>	external with L24	9	<u>L26</u>
<u>L25</u>	external with cpu\$1 with L24	6	<u>L25</u>
L24	L22 or L23	111	<u>L24</u>
<u>L23</u>	shared DSP\$1	83	<u>L23</u>
<u>L22</u>	shared digital signal processor\$1	30	<u>L22</u>
<u>L21</u>	shared DSP	79	<u>L21</u>
<u>L20</u>	("20030197365").PN.	1	<u>L20</u>
<u>L19</u>	microcomputer.ti. and L16	27	<u>L19</u>
<u>L18</u>	microcomputer and L16	54	<u>L18</u>
<u>L17</u>	microputer and L16	0	<u>L17</u>
<u>L16</u>	hitachi.as. and L15	59	<u>L16</u>
<u>L15</u>	(shiro and baba).in.	102	<u>L15</u>
<u>L14</u>	graphics and L13	55	<u>L14</u>
<u>L13</u>	L2 and L12	141	<u>L13</u>
<u>L12</u>	arm processor\$1	1230	<u>L12</u>
<u>L11</u>	L6 not L10	37	<u>L11</u>
<u>L10</u>	(L7 with L8) and L9	5	<u>L10</u>
<u>L9</u>	cellular phone	50332	<u>L9</u>
<u>L8</u>	"internal processor"	4018	<u>L8</u>
<u>L7</u>	"external processor"	7978	<u>L7</u>
<u>L6</u>	L2 and L3 and L4	42	<u>L6</u>
<u>L5</u>	L2 and L3 and 14L4	0	<u>L5</u>
<u>L4</u>	external processor\$1	8414	<u>L4</u>
<u>L3</u>	internal processor\$1	4320	<u>L3</u>
<u>L2</u>	cellular phone\$1	69344	<u>L2</u>
<u>L1</u>	cellular phone	50332	<u>L1</u>

END OF SEARCH HISTORY